



0015.0010

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of: FURUHATA )  
Serial No.: 09/459,305 )  
Filed: December 10, 1999 )  
For: SEMICONDUCTOR DEVICES, METHODS OF )  
MANUFACTURING SEMICONDUCTOR )  
DEVICES, CIRCUIT SUBSTRATES AND )  
ELECTRONIC DEVICES )

Group Art Unit: 2811  
Examiner: Tran, T.

**COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE**

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sirs:

In the statements of reasons for allowance the Examiner provided various reasons for allowance. Applicant notes that the claims are directed to various combinations of features. It is respectfully submitted that the patentability of each of the allowed claims resides in the combination of features recited in that claim in addition to any features noted by the Examiner.

Applicant also thanks the Examiner for the telephone discussion on July 20, 2004 in which the Examiner indicated that the drawings were accepted by the Examiner. The Examiner also acknowledged the claim for foreign priority and receipt of the priority document (as indicated in the Office Action dated Sept. 25, 2001 (paper no. 12)).

It is believed that no fee is due for consideration of this paper. If, however, a fee is due for consideration of this paper, please charge the fee to Deposit Account No. 50-0585.

Respectfully submitted,

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July 22, 2004

Date

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**Certificate of Mailing**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail on July 22, 2004 and is addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Alan S. Raynes

July 22, 2004

(Date)